




**Miniature receiver for reception of frequency or phase modulated RF signals**

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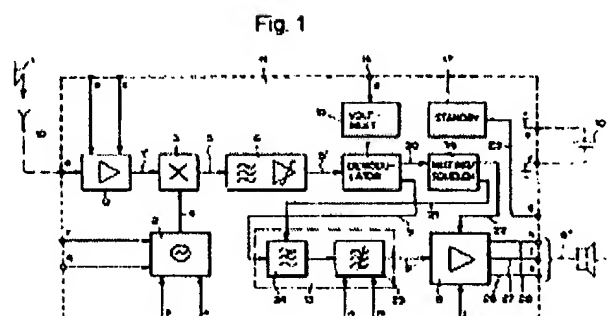
**Cited documents:**

 DE 3826294  
 DE 3032311

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**Abstract of EP0671818**

A single integrated circuit (11) in combination type BiCMOS technology combines a high-frequency amplifier (12), crystal-controlled oscillator (2), mixer (3), intermediate-frequency filter-amplifier (6), demodulator (7), low-pass filter (13) and low-frequency amplifier (8). A muting or squelch circuit (14) is also incorporated with a voltage multiplier (15) and a standby circuit (17). The LF sections implemented in CMOS technology consume very little current and can be supplied at 1.3 V from a single cell (10). The voltage multiplier provides the necessary extra power for the HF sections.



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